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### **Razavi-3930640 raz24936`FM`00i-xviii December 18, 201510:37 i**

CMOS Analog Circuit Design © P.E. Allen - 2016 DESIGN EXAMPLE OF A TWO-STAGE OP AMP  
Example 23-1 - Design of a Two-Stage Op Amp If  $K_{N'} = 120 \mu\text{A}/\text{V}^2$ ,  $K_{P'} = 25 \mu\text{A}/\text{V}^2$ ,  $V_{TN} = |V_{TP}| = 0.5 \pm 0.15\text{V}$ ,  $N = 0.06\text{V}^{-1}$ , and  $P = 0.08\text{V}^{-1}$ , design a two-stage, CMOS op amp that meets the following specifications.

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